

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	:	
	:	
Claude PETIT et al.	:	
	:	Examiner:
	:	
Serial No.: To be assigned.	:	Group Art Unit:
	:	
Filed: October 27, 2003	:	
	:	Divisional of
For: DEVICE AND METHOD FOR	:	Serial No. 10/173,751
MOUNTING INTEGRATED CIRCUITS	:	
ON A PRINTED CIRCUIT CARD	:	
	:	McLean, Virginia

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure set forth in 37 CFR 1.56, it is requested that the documents listed on the attached Information Disclosure Statement By Applicant be made of record in this application. These documents were cited in the corresponding French application No. FR01/0-8008 filed on June 19, 2001. A copy of the "Rapport De Recherche Preliminaire" (Preliminary Search Report) is enclosed. In compliance with the requirements of 37 CFR 1.97 and 1.98, the possible relevance of the documents is noted on the attached Information Disclosure Statement by Applicant. Copies of the documents are enclosed herewith.

Without any assertion of prior art effect, the documents on the attached Information Disclosure Statement by Applicant are called to the Examiner's attention.

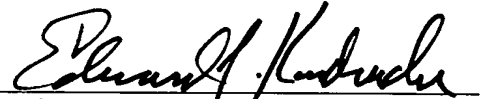
Although the relevance of each document has been briefly indicated, the Examiner is urged to make his own determination of relevance and no representation is made nor intended that "better" art is not available.

Respectfully submitted,

Miles & Stockbridge P.C.

Date October 27, 2003

By:



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Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.